Binho Nova Multi-Protocol USB Host Adapter

Product Datasheet

Revision 1.4

June 19, 2020
1  Key Features

- Support for SPI @ 12MHz max clock
- Support for I2C @ 3.4MHz max clock
- Support for UART @ 1000000 max baud
- Support for Dallas 1-Wire
- Support for Atmel Single-Wire Interface
- Provides 3v3 and $V_{\text{USB}}$ power rails
- 1 x DAC Output, 5 x ADC Inputs
- GPIO / Interrupt / PWM Support
- Programmable RGB Status LED
- Field-Upgradeable Device Firmware
- Cross-platform Support for Windows, Mac, Linux
- Robust, low-profile Aluminum Enclosure
- USB Type-C Connector

The Binho Nova features 5 signal pins, 1 x 3v3 pin, 1 x $V_{\text{USB}}$ pin, and 3 x GND pins on its 10pin wire harness. The wire harness terminates with a female 1.27mm 2x5 IDC connector. In IO Mode, the 5 signal pins can be used for varying functions such as Digital Input, Digital Output, PWM Output, Digital Interrupt (on rising edge, falling edge, or change), Analog Input, or Analog Output.

Additionally, the host adapter is able to utilize these pins to communicate on several digital buses: I2C, SPI, UART, (Dallas)1-Wire, and (Atmel) Single-Wire Interface. While in these modes of operation, remaining available pins can be assigned to other related or unrelated purposes such as gpio, interrupts, chip selects, PWM signals, or analog input or outputs.

2  Applications

- Firmware Development
- Proof of Concept Development
- System Debugging
- Automated Hardware Testing
- Automated Firmware Testing
- EEPROM Programming
- FLASH Programming
- FRAM Programming

The Binho Nova is ideal for manual testing during firmware development and debugging as well as a perfect way to automate hardware testing and validation. A common use-case of this product in production environments is for EEPROM/Flash Memory programming along with functional testing activities.

3  Description

The Binho Nova Multi-Protocol USB Host Adapter allows one to interface their computer directly to hardware circuits. This device is powered by the USB connection to the host PC and is also able to provide downstream power to test circuits.

4  Included Components

The Binho Nova comes along with a Breadboard Breakout Adapter and 1 foot USB type C (male) to type A (male) cable in a soft-shell zippered case.
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6 Pin Configuration

<table>
<thead>
<tr>
<th>Pin Name</th>
<th>Pin Number</th>
<th>Pin Function(s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>IO0</td>
<td>1</td>
<td>SDA (I2C), Digital In, Digital Out, Analog In, PWM</td>
</tr>
<tr>
<td>IO1</td>
<td>3</td>
<td>Digital In, Digital Out, Analog In, Analog Out, Interrupt</td>
</tr>
<tr>
<td>IO2</td>
<td>5</td>
<td>SDO (SPI), SCL (I2C), Digital In, Digital Out, Analog In, Interrupt, PWM</td>
</tr>
<tr>
<td>IO3</td>
<td>7</td>
<td>SCK (SPI), RX (UART), Digital In, Digital Out, Analog In, Interrupt, PWM</td>
</tr>
<tr>
<td>IO4</td>
<td>9</td>
<td>SDI (SPI), TX (UART), Digital In, Digital Out, Analog In, Interrupt, PWM</td>
</tr>
<tr>
<td>3V3</td>
<td>4</td>
<td>3.3V power rail</td>
</tr>
<tr>
<td>5V / V_USB</td>
<td>8</td>
<td>V_USB (typically 5V) power rail</td>
</tr>
<tr>
<td>GND</td>
<td>2, 6, 10</td>
<td>Ground connection</td>
</tr>
</tbody>
</table>

Note:

Dallas 1-WIRE and Atmel SWI (Single-Wire Interface) protocol can be configured to work with any of the five IO pins. It is especially convenient to use with IO0 or IO2 as it's possible to engage a suitable internal pull up resistor on these channels.
7 Electrical Specifications

7.1 Absolute Maximum Ratings

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>3v3 Rail Current</td>
<td></td>
<td>100</td>
<td>mA</td>
</tr>
<tr>
<td>5V / $V_{USB}$ Rail Current</td>
<td></td>
<td>200</td>
<td>mA</td>
</tr>
<tr>
<td>IO pin Current</td>
<td></td>
<td>10</td>
<td>mA</td>
</tr>
<tr>
<td>IO pin Voltage</td>
<td>-0.3</td>
<td>3.6</td>
<td>V</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>-25</td>
<td>85</td>
<td>°C</td>
</tr>
</tbody>
</table>

7.2 ESD Rating

The Binho Nova Multi-Protocol USB Host Adapter features IEC-61000-4-2 Level 4 ESD protection up to +/- 15KV contact and air discharge.

7.3 Recommended Operating Conditions

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>3v3 Rail Current</td>
<td></td>
<td>50</td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>5V / $V_{USB}$ Rail Current</td>
<td></td>
<td>100</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td>IO pin Current</td>
<td></td>
<td>2</td>
<td>7</td>
<td>mA</td>
</tr>
<tr>
<td>IO pin Voltage</td>
<td>0</td>
<td>3.3</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>0</td>
<td>25</td>
<td>80</td>
<td>°C</td>
</tr>
</tbody>
</table>
7.4 Electrical Characteristics

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{il}$ Input low-level Voltage</td>
<td></td>
<td></td>
<td>1.00</td>
<td>V</td>
</tr>
<tr>
<td>$V_{ih}$ Input high-level Voltage</td>
<td>1.82</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>$V_{ol}$ Output low-level Voltage</td>
<td></td>
<td>0.3</td>
<td>0.6</td>
<td>V</td>
</tr>
<tr>
<td>$V_{oh}$ Output high-level Voltage</td>
<td>2.64</td>
<td>2.97</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>ADC Resolution</td>
<td></td>
<td></td>
<td>12</td>
<td>bits</td>
</tr>
<tr>
<td>DAC Resolution</td>
<td></td>
<td></td>
<td>10</td>
<td>bits</td>
</tr>
</tbody>
</table>

7.5 RoHS Compliance

The Binho Nova Multi-Protocol USB Host Adapter is compliant to RoHS Directive 2011/65/EU of the European Parliament and the Council from 08/06/2011 on restriction of the use of certain hazardous substances in electrical and electronic appliances. The formal RoHS Declaration can be downloaded from our support website.
8 Application Information

***NOTICE***

The best source of the latest and most up-to-date information regarding the features of this product and the very specifics of how to operate the device can be found on our customer support portal. This support portal features a full set of comprehensive documentation and can be found at https://support.binho.io. More details on all of the items below can be found in the support portal.

8.1 Device Firmware Update

The Binho Nova Multi-Protocol USB Host Adapter features field-upgradeable firmware over USB. This allows for new features to be developed and released to all devices, such as support for new protocols.

8.2 Cross-Platform Support

The Binho Nova works well on all modern PC operating systems, such as Windows, Mac OS, and Ubuntu (Linux). One way in which this is achieved is by implementing the USB connection as a CDC (Virtual COM Port) device, and as a mass-storage device while performing a firmware update. This means there is no need to install custom device drivers, allowing the device to be used on any operating system which provides a system driver for these device classes. Additionally, numerous examples and tools are provided as open-source python scripts which can be run on any platform.

8.3 ASCII Command Set

The Binho Nova is controlled via a human-readable ASCII command set. The commands and responses were intelligently designed for easy use via manual entry in a serial console program and at the same time are robust enough for effective automation.

8.4 Multi-Adapter/Gang-able Operation

Binho Nova Multi-Protocol USB Host Adapters can be operated in gang-able fashion with many units connected to the same host PC. This allows for making complicated automated test fixtures or custom gang programming stations a breeze. Many examples exist demonstrating how easy it is to work with multiple devices.

8.5 Programmable RGB Status LED

The Binho Nova features a programmable RGB status LED. This can be used to distinguish a certain adapter when used in a multi-adapter setup/test fixture, to indicate that a particular part of the test script is being performed, or anything else you can imagine.
8.6 Digital IO

Each of the five IO pins on the Binho Nova Multi-Protocol USB Host Adapter can be used as a digital input or digital output pin. Some pins also feature the ability to operate as an interrupt pin, which can be configured to fire on rising edge, falling edge, or both. Furthermore, some pins also feature the ability to provide a PWM output.

8.7 Analog IO

Each of the five IO pins on the Binho Nova can be used as an analog input pin. These pins go to a 12-bit ADC (VREF=3.3V). Please note that the ADC is not calibrated, and this functionality is not meant to serve as an oscilloscope or other precise piece of analog test equipment. It is not characterized for performance on AC signals and should be considered suitable for measurement of ~DC signals. Additionally, there is one pin that is capable of generating an analog output signal from the internal 10-bit DAC.
9 Supported Protocols

***NOTICE***

The best source of the latest and most up-to-date information regarding the supported protocols and the very specifics of how to use them can be found on our customer support portal. This support portal features a full set of comprehensive documentation and can be found at https://support.binho.io. More details on all of the items below can be found in the support portal.

The Binho Nova documentation has recently been updated to replace legacy industry terms with modern terminology. Please see our customer support portal for additional clarity on modern naming conventions.

9.1 I2C Protocol

Note: The usage of I2C Master/Slave terminology is considered obsolete. Master and Slave are now Controller and Peripheral.

The Binho Nova supports communication as either an I2C Controller or I2C Peripheral device. The I2C CLK frequency is programmable from 100kHz to 3.4MHz in 1kHz steps. The device also features internal 2.2kOhm pull-up resistors on the SCL and SDA signals which can be programmatically engaged/disengaged as desired. While working in I2C mode, it’s possible to use the other 3 available pins as desired. Support for I2C Peripheral operation was introduced with firmware version 0.2.0 and is available to all existing devices via firmware update.

9.2 SPI Protocol

Note: The usage of SPI Master/Slave terminology is considered obsolete. Master and Slave are now Controller and Peripheral. As such, the usage of MOSI/MISO/SS for pin and signal names has been replaced with SDI/SDO/CS.

The Binho Nova supports communication as a SPI Controller device. Support for SPI Peripheral is coming soon. The SPI SCL frequency is programmable from 1MHz to 12MHz in 1kHz steps. While working as an SPI Controller, it’s possible to use the available pins for either 1) CS pin and INT pin, 2) CS0 pin and CS1 pin to support communication with 2 SPI Peripherals, or 3) any combination of CS, INT, or other available pin function. Support for SPI Peripheral operation is coming soon and will be made available to all existing devices via firmware update.

9.3 UART Protocol

The Binho Nova supports communication as a UART pass-through, supporting baud rates up to 1000000bps. This allows communication over the Virtual COM port as if it were a typical USB to UART adapter. Note that the use of hardware flow control (CTS/RTS signals) is not supported. While working in
UART mode, it’s possible to use the other 3 available pins as desired.

### 9.4 1-WIRE Protocol

The Binho Nova supports communication as a Dallas 1-WIRE Host device. This protocol can be configured to work on any of the five IO pins. It is especially convenient to use with IO0 or IO2 as it’s possible to engage a suitable internal pull up resistor on these channels, eliminating the need for an external resistor.

### 9.5 Single-Wire Interface Protocol

The Binho Nova Multi-Protocol USB Host Adapter supports communication as an Atmel SWI (Single-Wire Interface) Host device. This protocol can be configured to work on any of the five IO pins. It is especially convenient to use with IO0 or IO2 as it’s possible to engage a suitable internal pull up resistor on these channels, eliminating the need for an external resistor.
10 Mechanical Drawing

Binho Multi-Protocol USB Host Adapter Dimensioned Drawing
# 11 Revision Log

<table>
<thead>
<tr>
<th>Rev</th>
<th>Description of Changes</th>
<th>Date</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.0 [Draft]</td>
<td>Initial Draft</td>
<td>4/24/19</td>
</tr>
<tr>
<td>1.1</td>
<td>Added figures and missing values. Updated Max I2C CLK Frequency to 3.4MHz</td>
<td>6/11/19</td>
</tr>
<tr>
<td>1.2</td>
<td>Updated Mechanical Drawing To Improve Clarity</td>
<td>6/23/19</td>
</tr>
<tr>
<td>1.3</td>
<td>Updated text and titles to include “Nova” product name</td>
<td>1/3/2020</td>
</tr>
<tr>
<td>1.4</td>
<td>Replaced legacy protocol terminology with modern terms Updated Pinout Diagram Updated Section 9 “Supported Protocols”</td>
<td>6/19/2020</td>
</tr>
</tbody>
</table>