

# LED COLOR

## General Description

APA102 is a type ic for the three-color White Diming control strip and string ,This ic using the CMOS process to provide three-color White LED output driver to adjust the output with 256 gray scale and 32 brightness adjustment ,APA with 2 signal output way , one is clock ,another is data, the clock and data is synchronized ,so that the crystal cascade piece of output movement is sychronized .

## Feature

- CMOS process,low voltage ,low consumption
- Synchronization of two-lane
- Choose positive output or negative White three-color LED output
- 8 bit(256 level) color set ,5bit(32 level) brightness adjustment
- Build-20ma constant current output
- With self-detection signal build in support for continuous oscillation PWM output ,can be maintained static screen

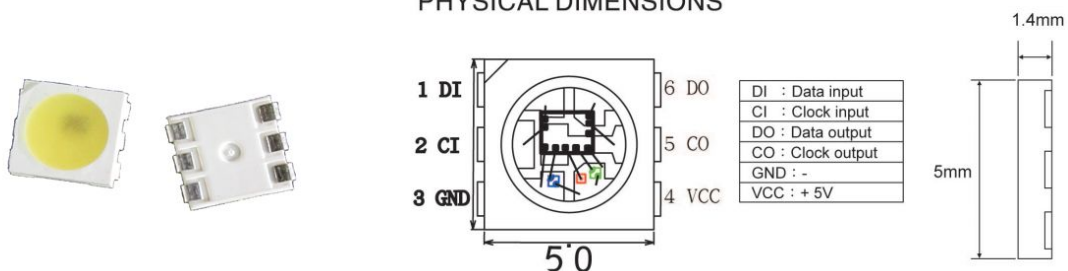
## Application

- LED lamp
- LED Strip ,led pixel module
- LED billboard LED screen

## PRODUCT SPECIFICATIONS

Color	LM(lumen)	Refresh rate	Voltage	Power Consumption	Weigh t (g)	An gle	Dimens ions (mm) L*W*H	Operating Temperat ure
White Warm white Cool white	18lm	400 cycle	DC5V	0.2W (MAX:1W )	1	16 0	5x5x1.4	-40-70°C

### PHYSICAL DIMENSIONS



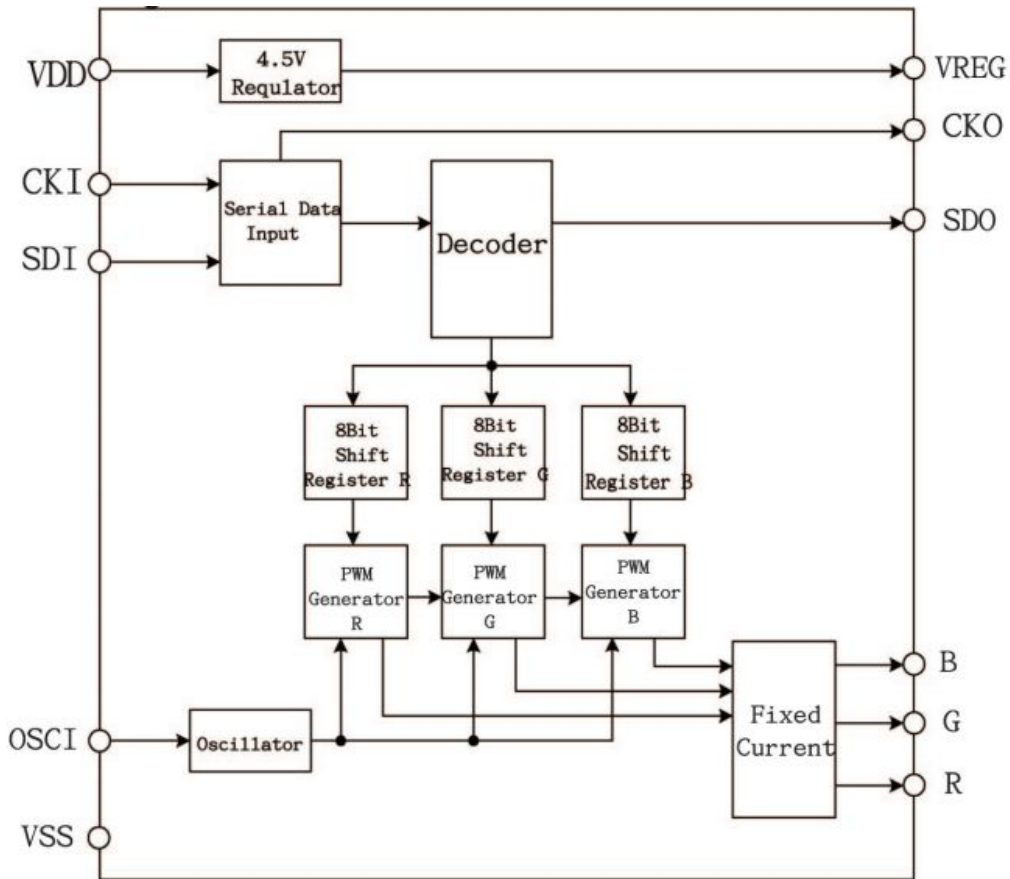
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## (PIN Description)

<b>NO.</b>	<b>PIN NAME</b>	<b>I/O</b>	<b>FUNCTION</b>
<b>1</b>	<b>VDD</b>	<b>P</b>	<b>Power is terminal</b>
<b>2</b>	<b>VREG</b>	<b>O</b>	<b>4.5V regulator output</b>
<b>3</b>	<b>CKO</b>	<b>O</b>	<b>Series with the output clock signal</b>
<b>4</b>	<b>SDO</b>	<b>O</b>	<b>Series with the output data</b>
<b>5</b>	<b>VEN</b>	<b>I</b>	<b>Self-test function selection</b>
<b>6</b>	<b>CSEL</b>	<b>I</b>	<b>Invert the clock signal cascade</b>
<b>7</b>	<b>POLAR</b>	<b>I</b>	<b>Positive and negative output options</b>
<b>8</b>	<b>OSCI</b>	<b>I</b>	<b>Oscillator input</b>
<b>9</b>	<b>SDI</b>	<b>I</b>	<b>Series with the input data</b>
<b>10</b>	<b>CKI</b>	<b>I</b>	<b>Series with the input clock signal</b>
<b>11</b>	<b>REXT</b>	<b>I</b>	<b>Constant current source to adjust side</b>
<b>12</b>	<b>VSS</b>	<b>P</b>	<b>Power supply negative terminal</b>
<b>13</b>	<b>W</b>	<b>O</b>	<b>W LED output</b>
<b>14</b>	<b>W</b>	<b>O</b>	<b>W LED output</b>
<b>15</b>	<b>W</b>	<b>O</b>	<b>W LED output</b>

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**(Block diagram)**



**(Absolute Maximum Rating)**

**Supply voltage ----- -0.3-6.0V**

**Input voltage----- VSS-0.3 to VDD +0.3V**

**Operating temperature----- -40 to +70°C**

**Storage temperature----- -50 to +125°C**

**Note:** Stress above those listed may cause permanent damage to the devices

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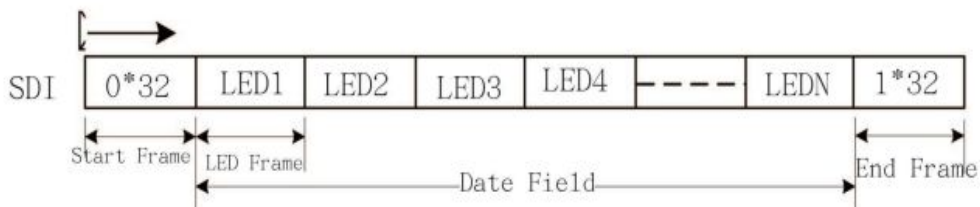
## (Electrical Characteristic)

Symbol	Parameter	Condition	Min.	Typ.	MAX	Units
VDD	Supply Voltage			5.0	5.5	V
VIH	Input High Voltage		0.7VDD		VDD+0.3	V
VH	Input Low Voltage		VSS-0.3		0.3VDD	V
LOL	Sink Current Voltage(RGB)	VDD=5V, VOL>1V	22.5	24.5	26.5	mA
RIN	Pull High	VDD=5V		570		kΩ
VREG	Regulator Voltage(VREG)	VDD=5V	4.4	4.5	4.7	V
FOSC	Oscillator Frequency		800		1200	KHz

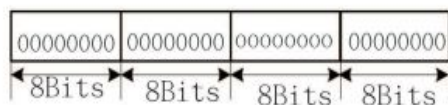
## (Function Description)

### (1) cascading data structure

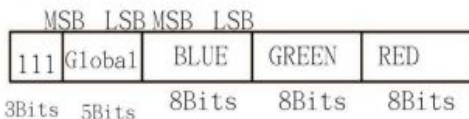
#### Tabdem N-LED



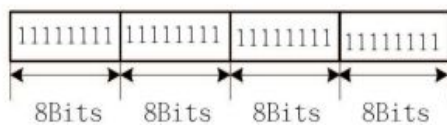
Start Frame 32 Bits



LED Frame 32 Bits



LED Frame 32 Bits

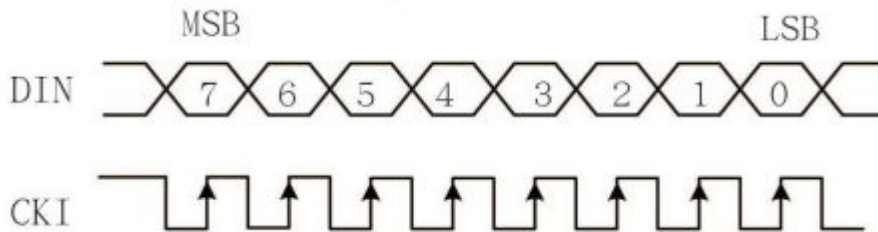


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Global bit:5 bit (32 level) brightness setting,while controlling W,W,W three-color constant current output value,if set the global bit for the 10000( 16/31 ) is the output current is half again the original PWM settings

DATA MSB←→LSB	Driving Current
00000	0/31
00001	1/31
00010	2/31
.....	
11110	30/31
11111	31/31(max)

## PWM input and output signals Relations



Data MSB--	Duty Cycle
00000000	0/256(min)
00000001	1/256
00000010	2/256
.....	
11111101	253/256
11111110	254/256
11111111	255/256(max)

(2). The number of pixel per second sent to the CKI frequency (FCKI) minus the Start Frame bit divided by the number 40 the number of LED Frame bit 32, if the CKI frequency(FCKI) to 512KHZ. The pixel number  $(512000-40)/32=15998$ , if the 50 second update Views can be connected in series LED number  $15998/50=319$ . To increase the number of cascaded IC CKI frequency

(3).POLAR to empty ,W,W,W for the negative output;POLAR access VSS ,W,W,W is positive output

(4).VEN: self-detection

DATA field to the middle of 3 bit is W,W,W in the MSB of the opposite phase,otherwise regarded as invalid data. VEN close to the empty when the self-detection; when VEN VSS then activated self-detection

(5).CSEL to empty when the CKO and CKI,RP:CSEL connected with VSS when the

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CKO compared with CKI.

## Application Circuit

